



Attorney's Docket No. 351891.03501 - PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Baolin Yang and Joel Phillips

Examiner: Kandasamy
Thangavelu

For: ***Method and Device for Multi-Interval
Chebyshev Collocation for Efficient High
Accuracy Circuit Simulation***

Group Art Unit: 2123

Application No.: 09/873,988

Filed: June 1, 2001

Commissioner for Patents
P.O. Box 1450
Arlington, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Applicant submits herewith patents, publications or other information (listed on the attached Form PTO-1449) of which Applicant is aware, which may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56.

In accordance with 37 CFR § 1.97(h), the filing of this Information Disclosure Statement shall not be construed to be an admission that the information cited in this statement is, or is considered to be, material to patentability as defined in 37 CFR § 1.56(b).

In accordance with 37 CFR § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR § 1.56(a) exists. It is submitted that the Information Disclosure Statement is in compliance with 37 CFR § 1.98 and MPEP § 609 and the Examiner is respectfully requested to consider the listed references.

The Commissioner is hereby authorized to charge **\$180.00** as well as any additional fees (or credit any overpayment) associated with this communication and which may be required under 37 CFR §1.16 or §1.17 to **Deposit Account No. 50-2603**, referencing **Attorney Docket No. 351891.03501**. **A duplicate sheet is attached.**

Respectfully submitted,

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Dated: 1/17/2005

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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(Use as many sheets as necessary)

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of

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Complete if Known

Application Number	09/873,988
Filing Date	June 1, 2001
First Named Inventor	Baolin Yang
Art Unit	2123
Examiner Name	Kandasamy Thangavelu
Attorney Docket Number	351891.03501

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	C1	DON, Wai Sun and SOLOMONOFF, Alex, "Accuracy and Speed in Computing the Chebyshev Collocation Derivative," <i>SIAM Journal on Scientific Computing</i> , November 1995, pp. 1253-1268, Vol. 16, No. 6, Society for Industrial and Applied Mathematics, Philadelphia, PA, USA.	
	C2	FORNBERG, Bengt, <i>A Practical Guide to Pseudospectral Methods</i> , February 1996, pp. 127-152, 155-160, Cambridge University Press, Cambridge, United Kingdom.	
	C3	GILMORE, Rowan J. and STEER, Michael B., "Nonlinear Circuit Analysis Using the Method of Harmonic Balance - A Review of the Art. Part I. Introductory Concepts," <i>International Journal of Microwave and Millimeter-Wave Computer-Aided Engineering</i> , 1991, pp. 22-37, Vol. 1, No. 1, John Wiley & Sons, Inc.	
	C4	GOTTLIEB, David and ORSZAG, Steven A., <i>Numerical Analysis of Spectral Methods: Theory and Applications</i> , 1977, pp. 1-19, 149-153, 159-161, Conference Board of the Mathematical Sciences.	
	C5	HAIRER, E. and WANNER, G., "Chapter IV. Stiff Problems - One-Step Methods," <i>Solving Ordinary Differential Equations II: Stiff and Differential-Algebraic Problems</i> , 1991, pp. 1-39, Springer-Verlag, Berlin, Germany.	
	C6	KUNDERT, Kenneth S., "Introduction to RF Simulation and Its Application," <i>IEEE Journal of Solid-State Circuits</i> , September 1999, pp. 1298-1319, Vol. 34, No. 9, IEEE.	
	C7	LONG, David et al., "Full-Chip Harmonic Balance," <i>IEEE 1997 Custom Integrated Circuits Conference</i> , May 1997, pp. 379-382, Vol. 16, No. 4, IEEE.	
	C8	NASTOV, Ognen J. and WHITE, Jacob K., "Time-Mapped Harmonic Balance," <i>Proceedings of the 36th Design Automation Conference</i> , New Orleans, Louisiana, June 1999, pp. 641-646, Vol. 36, No. 3, ACM.	
	C9	TELCHEVESKY, Ricardo et al., "Efficient AC and Noise Analysis of Two-Tone RF Circuits," <i>Proceedings of the 33rd Design Automation Conference</i> , June 1996, pp. 292-297, Vol. 21, No. 3, ACM.	
	C10	YANG, B. et al., "Spectral Simulations of Electromagnetic Wave Scattering," <i>Journal of Computational Physics</i> , July 1997, pp. 216-230, Vol. 134, No. 2, Academic Press Professional, Inc., San Diego, CA, USA.	

Examiner
SignatureDate
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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